

FORM PTO-1449  
(REV.7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
851763.406APPLICATION NO.  
09/843,533**INFORMATION DISCLOSURE STATEMENT**

(Use several sheets if necessary)

APPLICANTS

William Fornaciari et al.

FILING DATE

April 25, 2001

GROUP ART UNIT

2183

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AK					
	AL					
	AM					
	AN					
	AO					

**OTHER PRIOR ART** (Including Author, Title, Date, Pertinent Pages, Etc.)

wh	AP	Ramprasad et al., "A Coding Framework For Low-Power Address and Data Busses", <i>IEEE Transactions on VLSI</i> , Vol. 7 (2), pp 212-221
wh	AQ	Stan et al., "Low-Power Encodings For Global Communication in CMOS VLSI", <i>IEEE Transactions on VLSI</i> , Vol. 5 (4), pp 444-455
	AR	

EXAMINER

wim I. Hyngh

DATE CONSIDERED

11/23/03

\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).